



Patent

IFW

Case Docket No. T&A-135

In RE application of S. SAKAI et al

Serial No.: 10/519,799

Group Art Unit: 2891

For: METHOD OF PRODUCING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

Examiner: V.V. Yevsikov

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☐ Small entity of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.
- ☐ A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.
- ☐ No additional fee is required.

The fee has been calculated as shown below:

(Col. 1)			(Col. 2)		(Col. 3)	SMALL ENTITY		OR	OTHER THAN A SMALL ENTITY	
	Claims Remaining After Amendment		Highest No. Previously Paid For		Present Extra	Rate	Additional Fee		Rate	Additional Fee
Total	17	Minus	** 20	=		X 25	\$		X 50	\$
Indep.	2	Minus	*** 3	=		X 100	\$		X 200	\$
						X 180	\$		X 360	\$
<input type="checkbox"/> First presentation of Multiple Dependent Claims						Total	\$	OR	Total	\$

* If the entry in Col. 1 is less than the entry in Col. 2, write "0" in col. 3.
 ** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write '20' in this space.
 *** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write '3' in this space.
 The "Highest Number Previously Paid For" (Total or Independent) is the highest number found from the equivalent box in Col. 1 of a prior Amendment or the number of claims originally filed.

- ☐ Please charge my Deposit Account No. 50-1417 in the amount of \$_____.
- ☐ A Credit Card Payment Form in the amount of \$_____ is attached.
- ☐ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayments to Deposit Account No. 50-1417.
- ☒ Any filing fees under 37 CFR 1.16 for the presentation of extra claims.
- ☒ Any patent application processing fees under 37 CFR 1.17.
- ☒ Any Extension of Time fees that are necessary, which are hereby requested if necessary.

Mattingly, Stanger, Malur & Brundidge, P.C.
 1800 Diagonal Road, Suite 370
 Alexandria, Virginia 22312
 Tel: (703) 684-1120
 Fax: (703) 684-1157

By:


 Shrinath Malur, Reg. No. 34,663
 Attorney for Applicant(s)

Date: September 25, 2006



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Satoshi SAKAI et al.
Serial No.: 10/519,799
Filed: December 29, 2004
For: METHOD OF PRODUCING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Group: 2891
Examiner: V. V. Yevsikov
Docket No. : T&A-135
Customer No.: 24956

AMENDMENT

Commissioner for Patents
Mail Stop Amendment
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed June 23, 2006, please amend the above-identified application as follows.

Amendment to the Claims being on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.